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 Digital Object Identifier 10.1109/ICCD.2004.1347953
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- ☐ 2. **Simulation based architectural power estimation for PLA-based controlle**
 Katkoori, S.; Vemuri, R.;
 Low Power Electronics and Design, 1996., International Symposium on
 12-14 Aug. 1996 Page(s):121 - 124
 Digital Object Identifier 10.1109/LPE.1996.547492
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